# SMD POWER COIL-JNR 4012H-SERIES

### **Reliability and Test Condition**

Item	Performance	Test Condition			
Electrical Performance	Electrical Performance Test				
Inductance	Refer to standard electrical characteristics	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter			
RDC	list	CH16502,Agilent33420A Micro-Ohm Meter			
Saturation Current (Isat)	∆L≦30% typical.	Saturation DC Current (Isat) will cause L0 to drop $\triangle$ L(%)			
Heat Rated Current (Irms)	Approximately △T≦40°C	Heat Rated Current (Irms) will cause the coil temperature rise △T(°C) without core loss.  1.Applied the allowed DC current(keep 1 min.)  2.Temperature measured by digital surface thermometer			
Operating Temperature	-40°C ~+125°C (Including self - temperature	rise)			
Storage Temperature	-40~+125℃ (on board)				
Reliability Test					
Life Test		Preconditioning: Run through IR reflow for 2 times.  ( IPC/JEDEC J-STD-020D Classification Reflow Profiles)  Temperature: 125±2°C (Inductor)  Applied current: rated current  Duration: 1000±12hrs  Measured at room temperature after placing for 24±2 hrs			
Load Humidity		Preconditioning: Run through IR reflow for 2 times.  ( IPC/JEDEC J-STD-020D Classification Reflow Profiles)  Humidity: 85±2% R.H  Temperature: 85°C±2°C  Duration: 1000hrs Min. with 100% rated current  Measured at room temperature after placing for 24±2 hrs			
Moisture Resistance	Appearance: No damage Inductance: within±10% of initial value Q: Shall not exceed the specification value RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.  (IPC/JEDEC J-STD-020D Classification Reflow Profiles)  1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs.  2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs.  3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs, keep at 25°C for 2 hrs then keep at -10°C for 3 hrs  4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.			
Thermal shock		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: $-40\pm2^{\circ}\mathbb{C}$ 30 $\pm$ 5min Step2: $25\pm2^{\circ}\mathbb{C} \le 0.5$ min Step3: $125\pm2^{\circ}\mathbb{C} \le 0.5$ min Number of cycles: 500 Measured at room temperature after placing for 24 $\pm$ 2 hrs			
Vibration		Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations)			

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Shock	Appearance: No damage. Inductance: within±10% of initial value	Type         Peak value         Normal duration (D) (ms)         Wave form         Velocity           SMD         50         11         Half-sine         11.3           Lead         50         11         Half-sine         11.3
Bending	Q : Shall not exceed the specification value RDC : within ±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.
Soderability	More than 95% of the terminal electrode should be covered with solder	Preheat: 150°C,60sec Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C Flux for lead free: Rosin. 9.5% Dip time: 4±1sec Depth: completely cover the termination
Resistance to Soldering Heat  Terminal Strength	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value RDC: within ±15% of initial value and shall not exceed the specification value	Depth: completely cover the termination Number of heat cycles: 1  Temperature Time (°C) (s) and emersion rate  260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s  Preconditioning:Run through IR reflow for 2 times (IPC/JEDEC J-STD-020D Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a force (>0805 inch(2012mm):1kg, <=0805 inch(2012mm):0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds.  Also the force shall be applied gradually as not to apply a shock to the component being tested.

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Reliability Test			
Soldering	Mildly activated rosin fluxes are preferred. JANTEK terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.		
Lead Free Solder re-flow:	Recommended temperature profiles for re-flow soldering in Figure 1.		
Soldering Iron:	Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.  Note:  • Preheat circuit and products to 150°C  • Never contact the ceramic with the iron tip  • Use a 20 watt soldering iron with tip diameter of 1.0mm  • 355°C tip temperature (max)  • 1.0mm tip diameter (max)  • Limit soldering time to 4~5 sec		



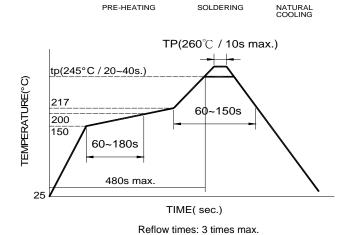


Fig.1

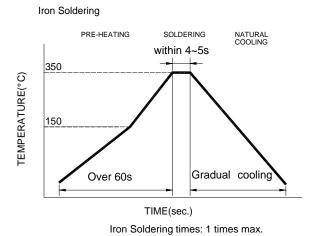


Fig.2